

SKY59608-711LF: Sky5® 2.4 to 8.3 GHz SPDT Switch

Applications

- Wi-Fi® 6E T/R switches
- WLAN repeaters
- UWB applications
- Low power transmit/receive systems
- Smartphones
- Connectivity modules

Features

- Broadband frequency range: 2.4 to 8.3 GHz
- Low insertion loss, 0.75 dB typical @ 5 to 7 GHz
- High isolation, 23 dB typical @ 5 to 7 GHz
- Excellent linearity performance, IP0.1dB = +31 dBm
- Single control logic
- 1.1 V and 3.6 V logic compatibility
- Wide 2.7 to 5 V supply voltage range
- 200 ns switching time
- Ultra-miniature, 6-pin, 1.1 x 0.7 x 0.45 mm exposed pad plastic Micro Lead-frame Package Dual (MLPD) package
- MSL1, 260 °C per JEDEC J-STD-020
- For RoHS and other product compliance information, see the [Skyworks Certificate of Conformance](#).

Description

The SKY59608-711LF is a single-pole, double-throw (SPDT) switch for mode switching in WLAN applications. Using advanced switching technologies, the SKY59608-711LF maintains low insertion loss and high isolation for all switching paths.

The SKY59608-711LF is part of our Sky5® product portfolio.

The high linearity performance and low insertion loss make it an ideal choice for low-power transmit/receive applications.

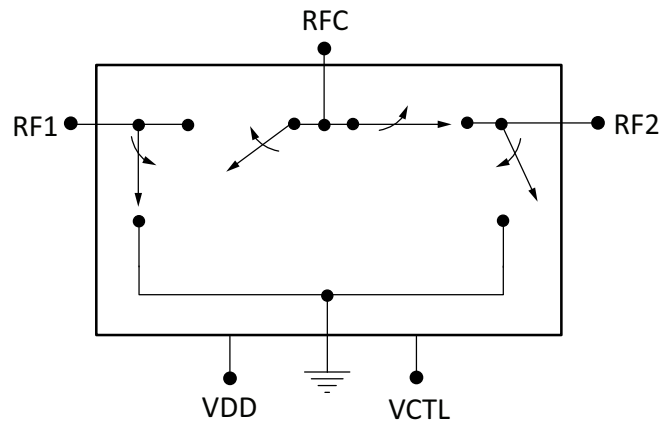


Figure 1. Functional Block Diagram

Depending on the logic voltage applied to the control pin (VCTL), the RFC pin is connected to one of the two switched RF outputs (RF1 or RF2) using a low insertion loss path, while the path between the RFC pin and the other RF pin is in a high-isolation state. The switch is a “reflective short” on the isolated port.

A functional block diagram is shown in Figure 1. The pin configuration is shown in Figure 2. Signal pin assignments and descriptions are provided in Table 1.

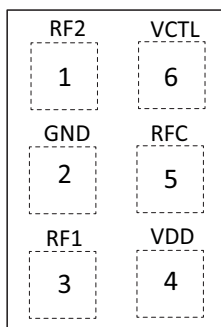


Figure 2. Pinout (Top View)

Table 1. Signal Descriptions

Pin	Name	Description	Pin	Name	Description
1	RF2	RF port	4	VDD	DC supply voltage
2	GND	Ground	5	RFC	RF common port
3	RF1	RF port	6	VCTL	Control pin

Electrical and Mechanical Specifications

Table 2. Absolute Maximum Ratings¹

Parameter	Symbol	Minimum	Maximum	Units
Input power	P_{IN}		+32	dBm
Supply voltage	V_{DD}		5.5	V
Control voltage	V_{CTL}		3.7	V
Storage temperature	T_{STG}	-65	+150	°C
Operating temperature	T_{OP}	-40	+90	°C

1. Exposure to maximum rating conditions for extended periods may reduce device reliability. Exceeding any of the limits listed here may result in permanent damage to the device.

ESD Handling: Industry-standard ESD handling precautions must be adhered to at all times to avoid damage to this device.

Table 3. Recommended Operating Conditions

Parameter	Symbol	Minimum	Typical	Maximum	Units
Operating frequency	f_o	2.4		8.3	GHz
Supply voltage	V_{DD}	2.7	3.3	5	V
Control voltage Low High	V_{CTL_L} V_{CTL_H}	0 1.1		0.4 3.6	V V
Operating temperature	T_{OP}		+25		°C

Table 4. Electrical Specifications¹(V_{DD} = 3.3 V, V_{CTL} = 0 V and 1.8 V, T_{OP} = +25 °C, P_{IN} = 0 dBm, characteristic impedance [Z_O] = 50 Ω, unless otherwise noted)

Parameter	Symbol	Test Condition	Minimum	Typical	Maximum	Units
Insertion loss	IL	2400 to 5000 MHz 5150 to 5925 MHz 5925 to 7125 MHz 7125 to 8300 MHz		0.5 0.6 0.75 0.8	0.85 1 1.2 1.4	dB dB dB dB
Isolation	ISO	2400 to 5000 MHz 5150 to 5925 MHz 5125 To 7125 MHz 7125 to 8300 MHz	25 22 19 17	28 26 23 20		dB dB dB dB
Input return loss	[S11]	5150 to 7125 MHz	10	14		dB
Output return loss	[S22]	5150 to 7125 MHz	10	14		dB
P0.1dB compression point	P0.1dB	5125 to 7125 MHz		+31		dBm
Harmonics	2fo	P _{IN} = +24 dBm: AX80-MCS0 fo = 5150 to 7125 MHz		-70	-60	dBm
	3fo	P _{IN} = +24 dBm: AX80-MCS0 fo = 5150 to 7125 MHz		-66	-55	dBm
Error vector magnitude	EVM	Pin = 24 dBm, AX80-MCS11, F0 = 5150 to 7125 MHz		-50		dB
IIP3	IIP3	Tone1 = Tone2 = 20 dBm Tone spacing = 10 MHz fo = 5150 to 7125 MHz	55	63		dBm
Turn on time	T _{ON}	Application of VDD to switch ready for use		1	10	μs
Switching speed	T _{SW}	50% VCTL to 90% RF		120	200	ns
Supply current	I _{DD}			15	30	μA
Control current	I _{CTRL}	V _{CTRL} = 1.1 to 2.0 V		0.3	10	μA
		V _{CTRL} = 3.3 V ²		2	10	μA

1. Performance is guaranteed only under the conditions listed in this table.

2. A voltage divider (650 kΩ/1 MΩ) is used.

Table 5. Truth Table¹

VDD (Pin 4)	VCTL (Pin 6)	RFC to RF1 Path	RFC to RF2 Path
1	0	Insertion loss	Isolation
1	1	Isolation	Insertion loss

1. "1" indicates VDD = 2.7 to 5 V, VCTL = 1.1 to 3.6 V. A voltage divider is recommended if VCTL is above 2.0 V.

"0" indicates VCTL = 0 to 0.4 V.

Any state other than described in this table places the switch into an undefined state. An undefined state will not damage the device.

Evaluation Board Description

An Evaluation Board (EVB) is used to test the performance of the SKY59608-711LF SPDT switch. An EVB schematic is provided in Figure 3. An assembly drawing for the Evaluation Board is shown in Figure 4.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY59608-711LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead- or lead-free soldering. For additional information, refer to the Skyworks Application Note, Solder Reflow Information, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

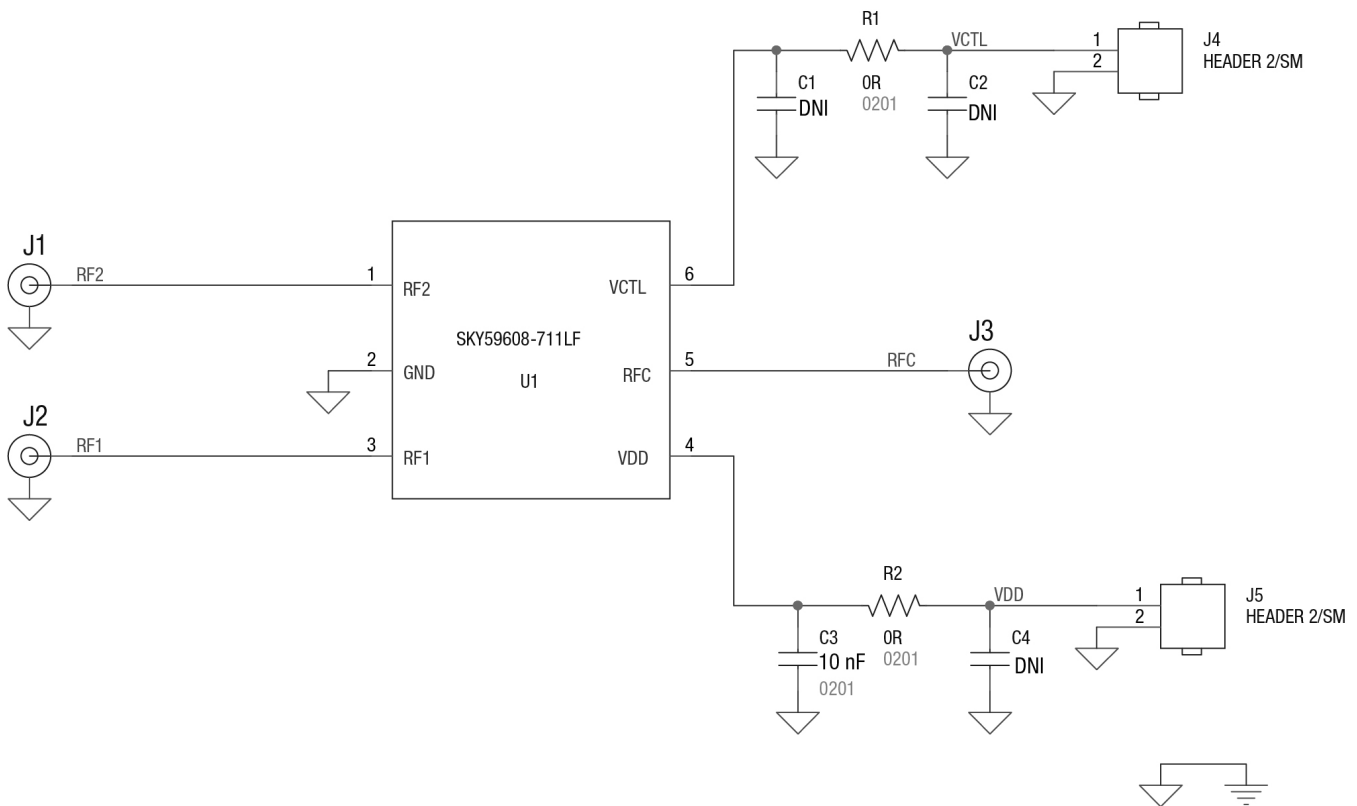


Figure 3. Evaluation Board Schematic

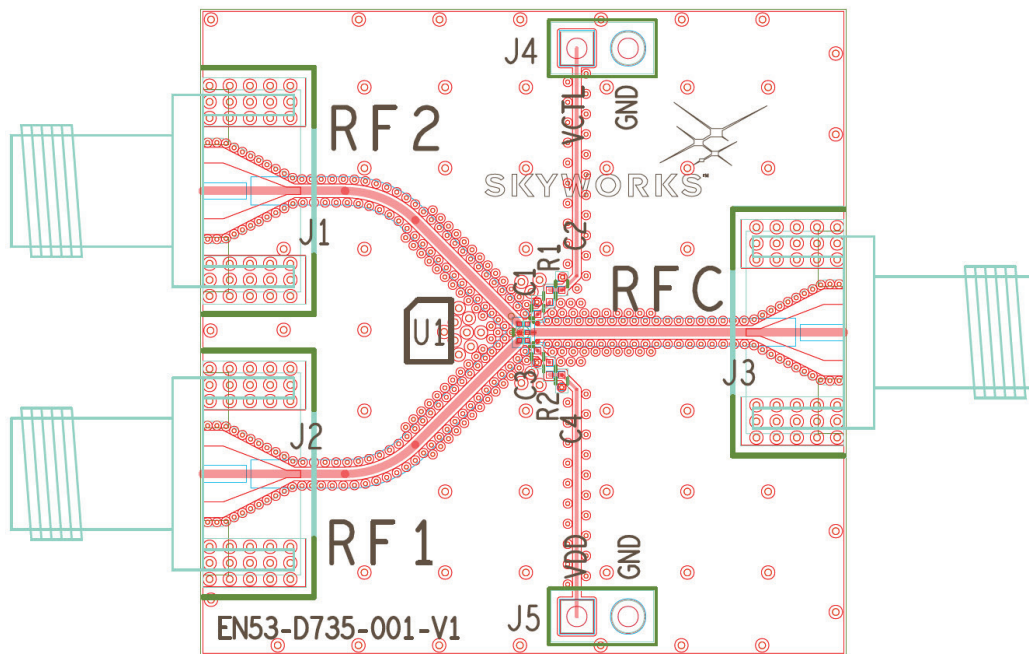
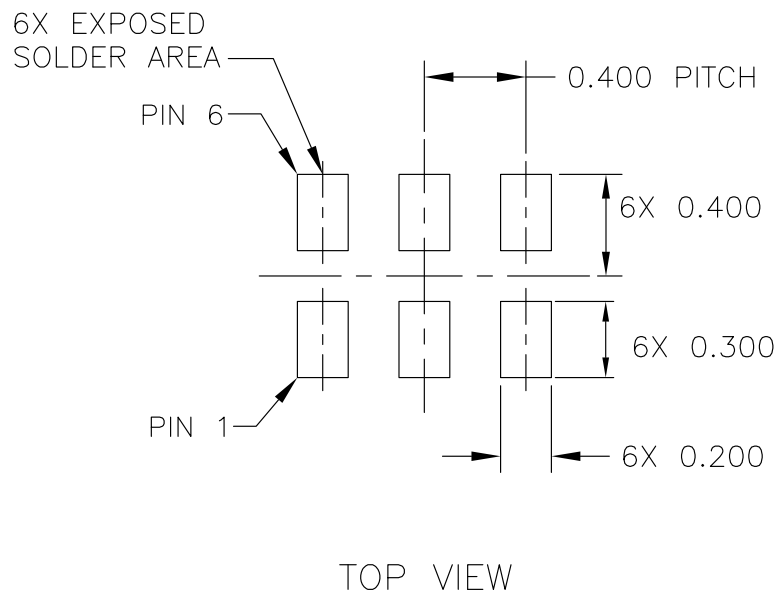


Figure 4. Evaluation Board Assembly Diagram



UNLESS OTHERWISE SPECIFIED
 DIMENSIONS ARE IN MILLIMETERS
 TOLERANCES ON FINISH: RMS 6.3
 ANGLES: $\pm 1/2^\circ$ FRACTIONS: $\pm 1/64$
 3 PLACE DECIMALS: ± 0.025
 2 PLACE DECIMALS: ± 0.05
 1 PLACE DECIMALS: ± 0.1

Figure 5. PCB Layout Footprint (Top View)

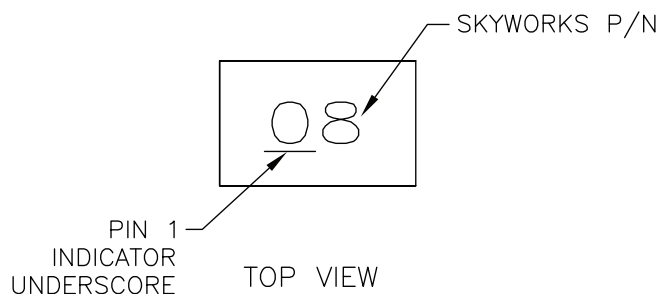
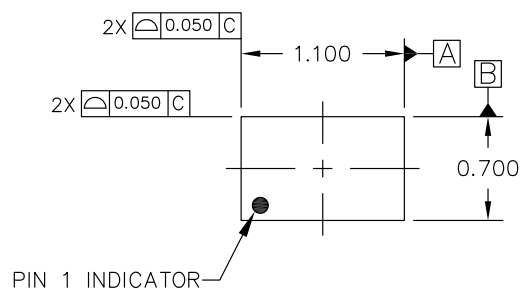
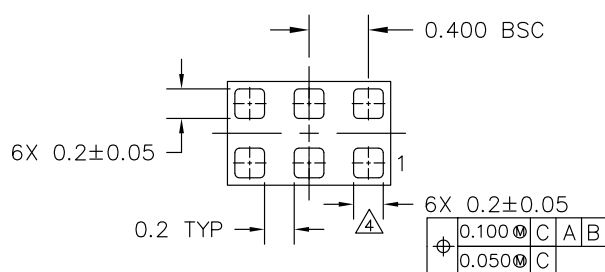


Figure 6. Typical Part Marking

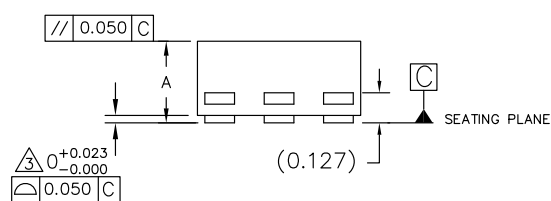


TOP VIEW



BOTTOM VIEW

A	MAX.	0.500
	NOM.	0.450
	MIN.	0.400



SIDE VIEW

NOTES:

1. PLATING REQUIREMENT PER SOURCE CONTROL DRAWING (SCD) 2504.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. COPLANARITY APPLIES TO THE TERMINALS AND ALL OTHER BOTTOM SURFACE METALLIZATION.
4. DIMENSION APPLIES TO METALLIZED TERMINAL. IF THE TERMINAL HAS A RADIUS ON ITS END, THE WIDTH DIMENSION SHOULD NOT BE MEASURED IN THAT RADIUS AREA.
5. ALL DIMENSIONS ARE IN MILLIMETERS.

Figure 7. Package Dimensions

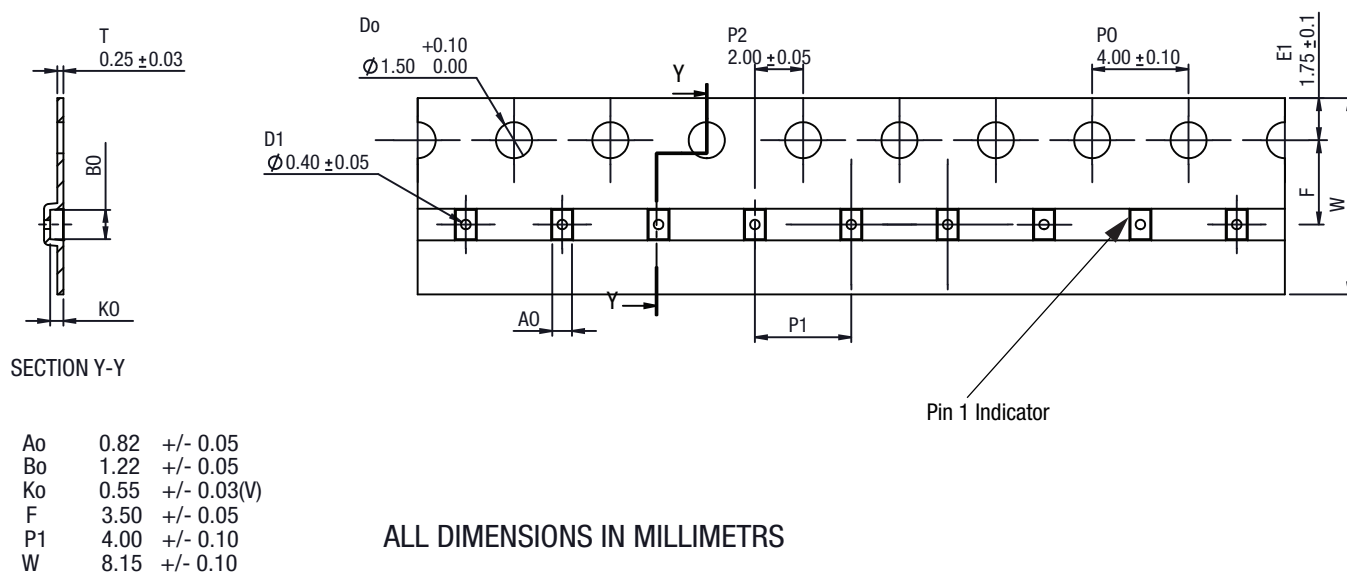


Figure 8. Tape and Reel Dimensions

Ordering Information

Part Number	Description	Evaluation Board Part Number
SKY59608-711LF	Sky5® 2.4 to 8.3 GHz SPDT Switch	SKY59608-711EK1

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